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Title: IN-BAND-FLAT-GROUP-DELAY TYPE DIELECTRIC FILTER AND LINEARIZED
AMPLIFIER USING THE SAME
Attorney Name: Douglas P. Mueller
Phone No.: (612) 371-5237
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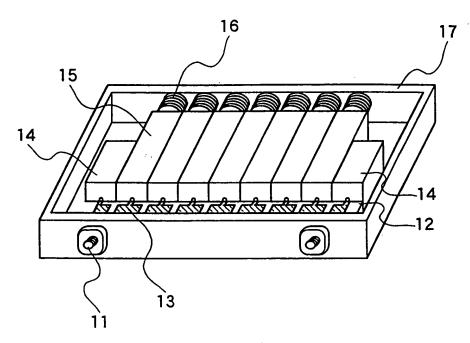


FIG. 1A

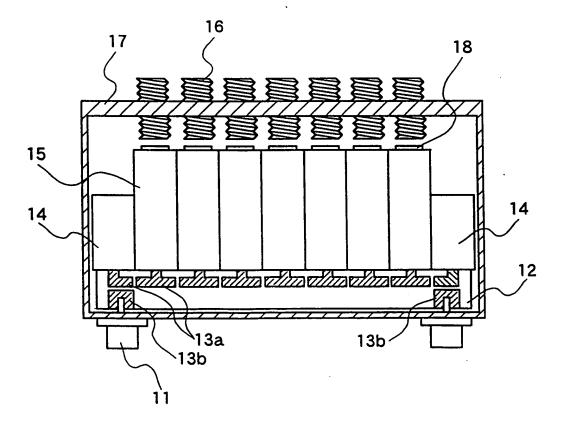


FIG. 1B

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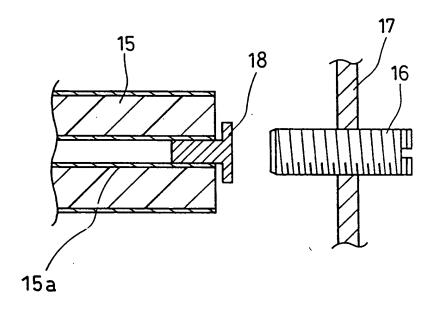


FIG. 2

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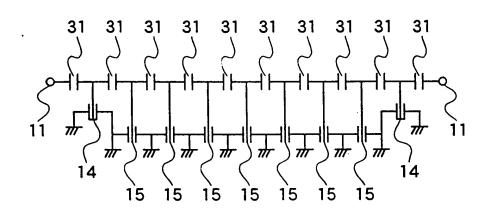
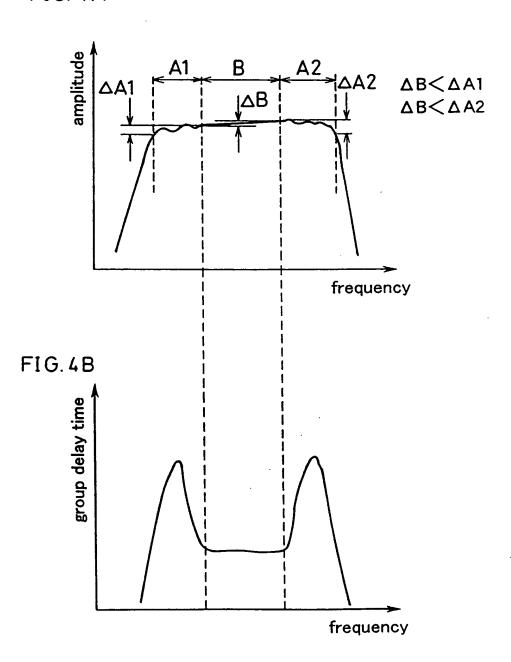


FIG. 3

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FIG. 4A



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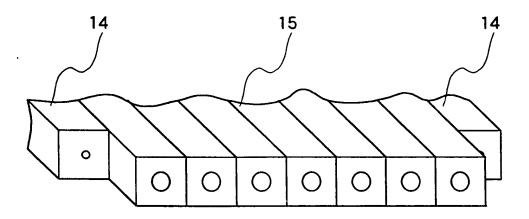


FIG. 5

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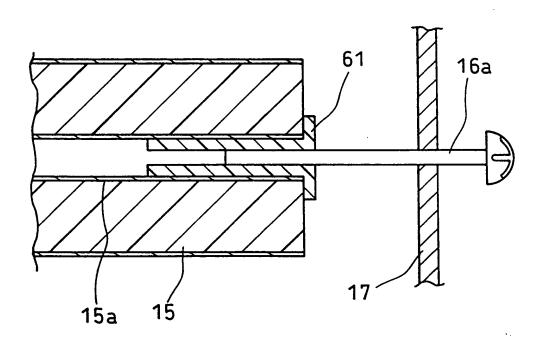
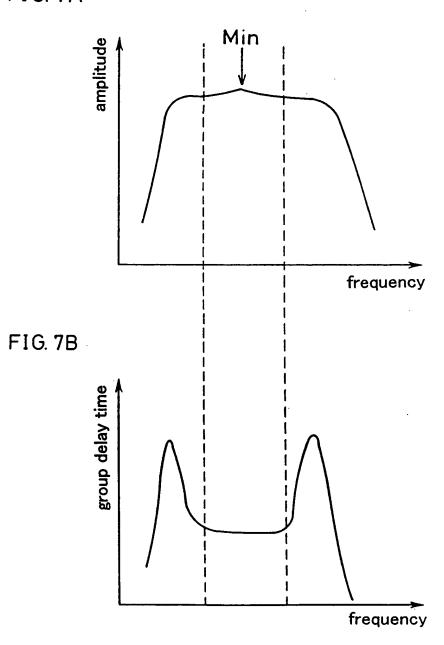


FIG. 6

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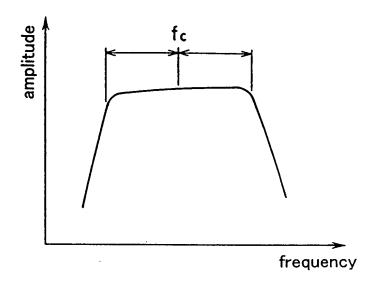
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FIG. 7A



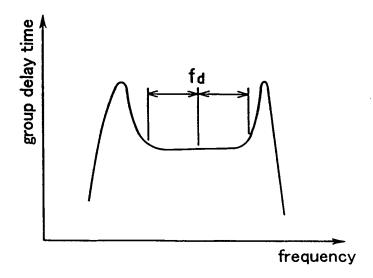
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FIG.8A



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FIG.8B



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FIG.9A

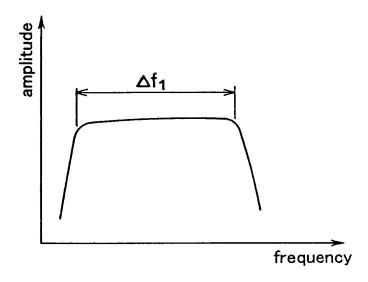
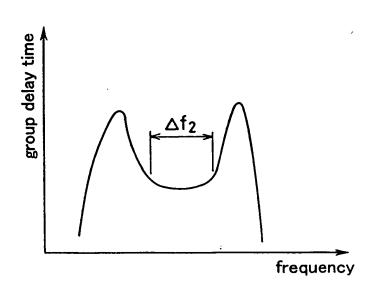


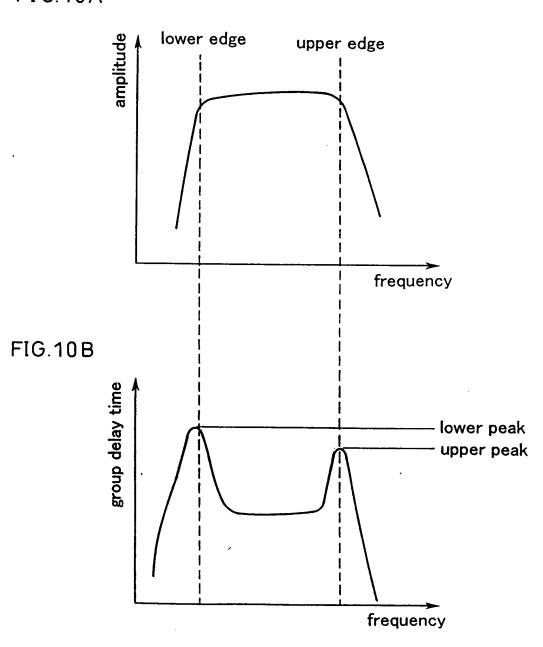
FIG.9B



 $\Delta f_1 > 2 \times \Delta f_2$

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FIG.10A



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FIG.11A

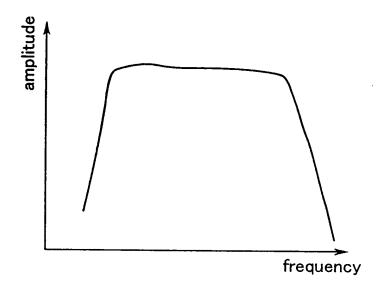
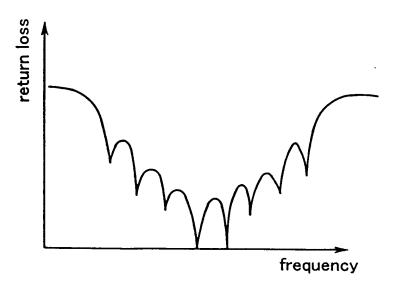


FIG.11B



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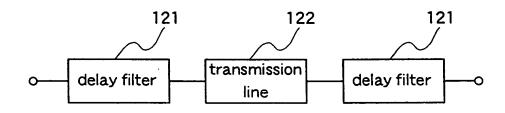


FIG. 12

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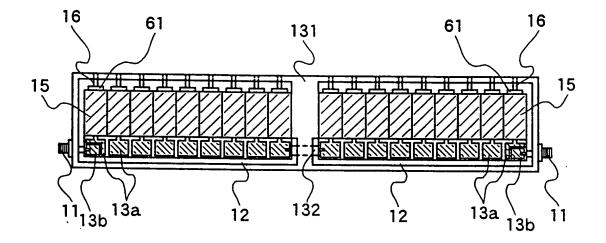


FIG. 13A

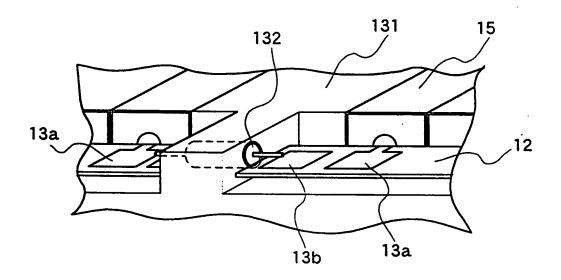
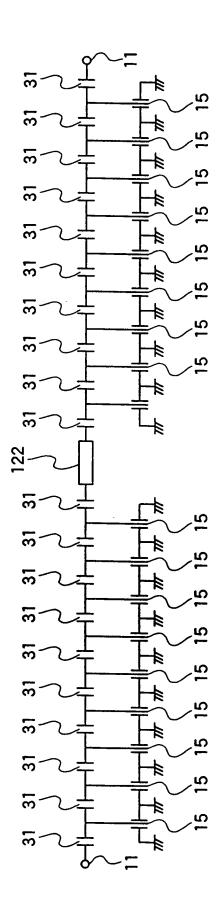


FIG. 13B

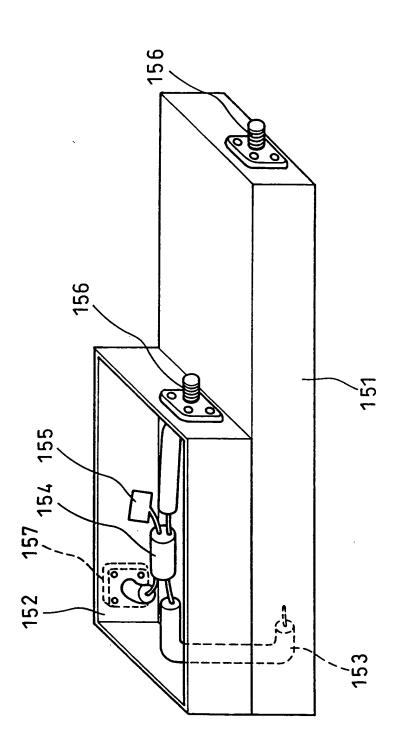
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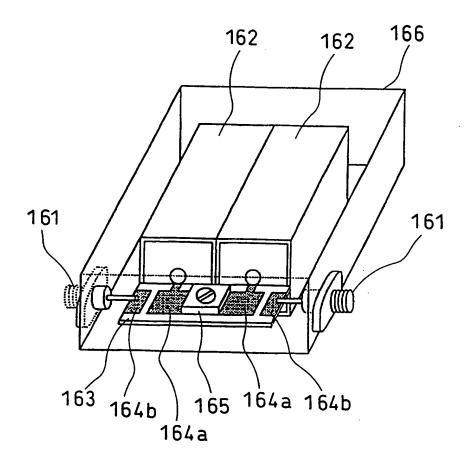
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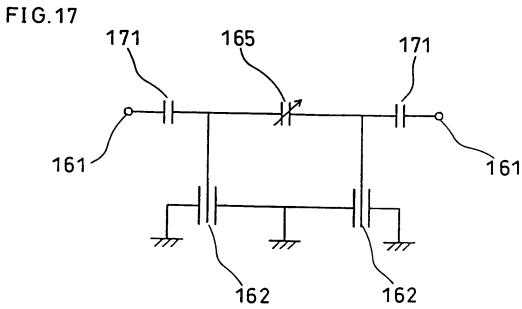
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FIG. 16





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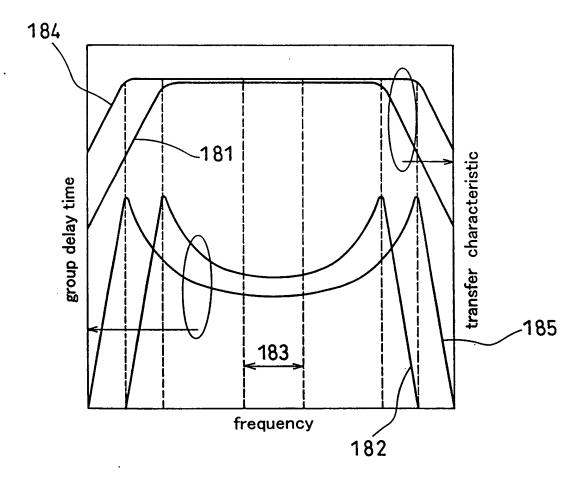
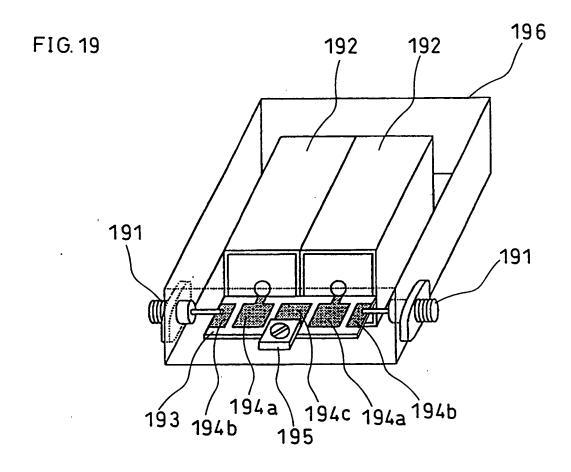
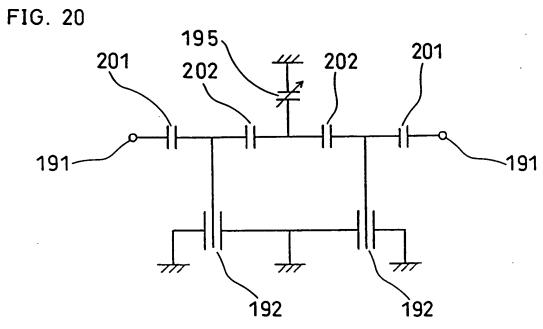


FIG. 18

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FIG. 21

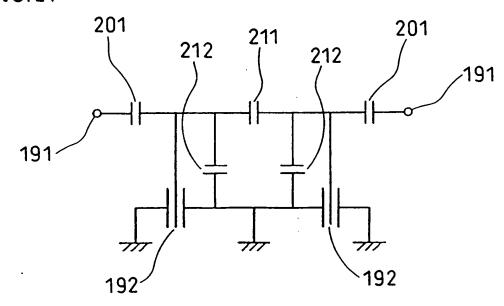
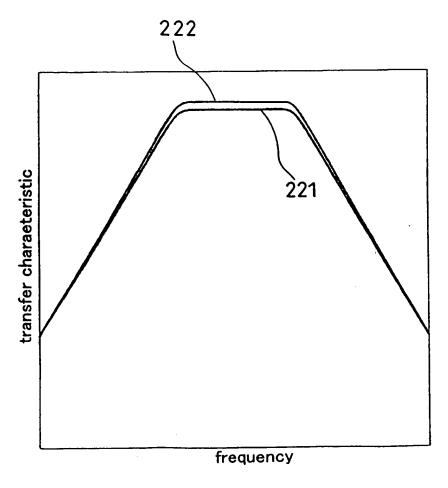


FIG. 22



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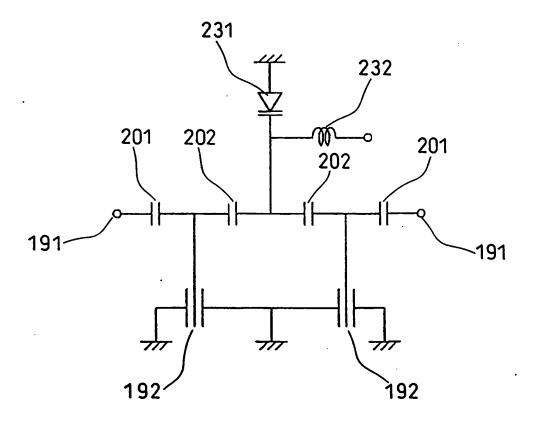
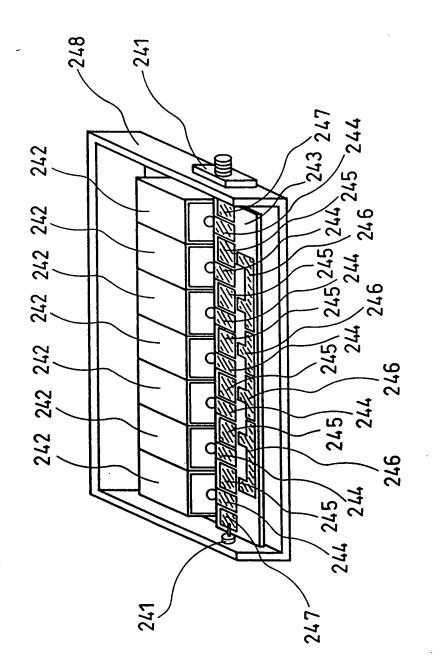


FIG. 23

¹Inventor: Yamakawa, et al Docket No.: 10873.559USD2

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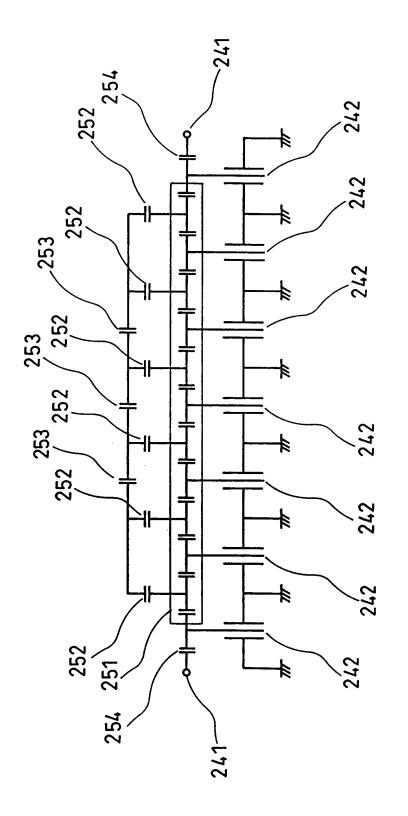


FIG. 25

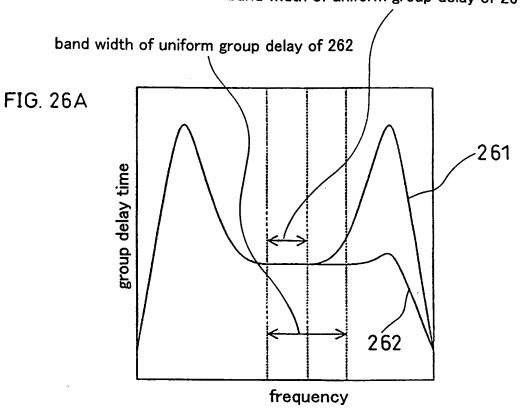
Inventor: Yamakawa, et al Docket No.: 10873.559USD2

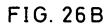
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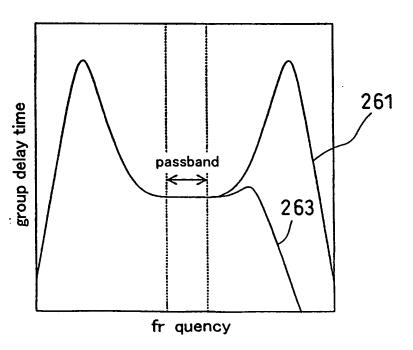
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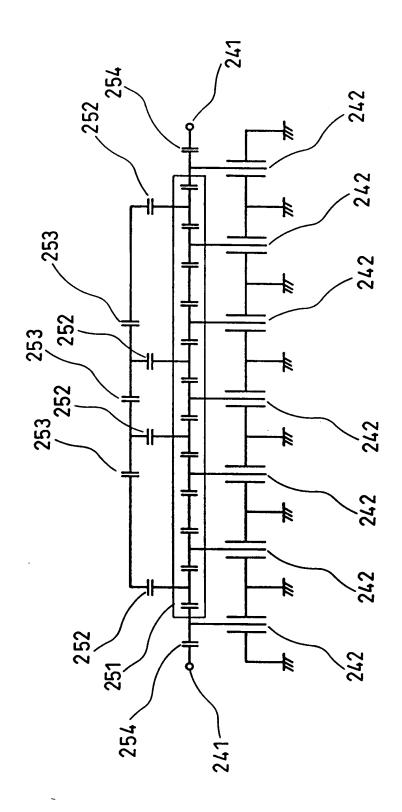




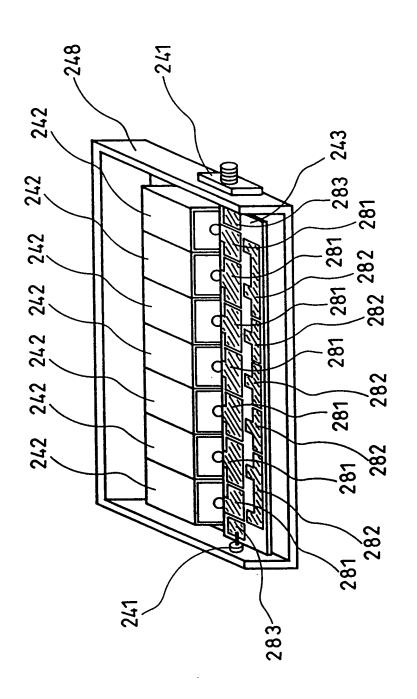


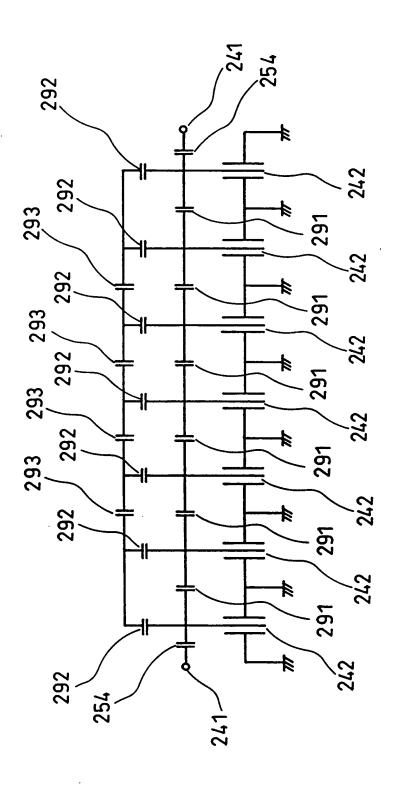
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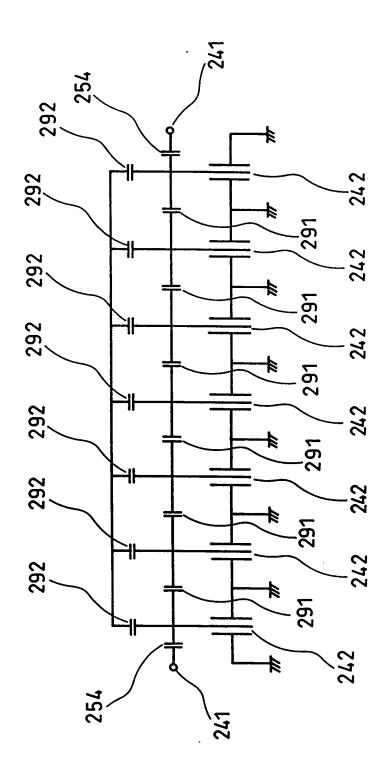
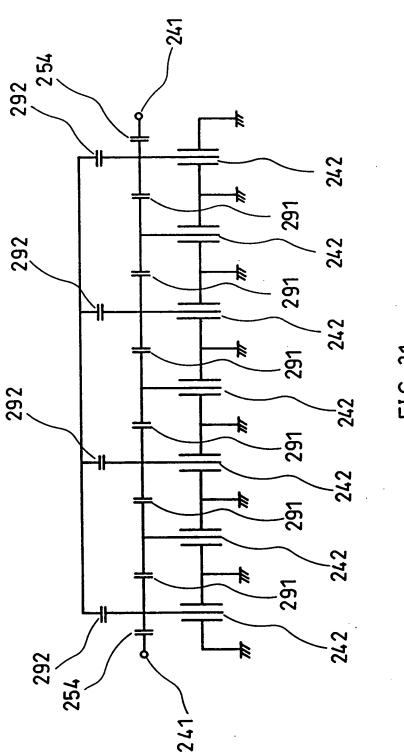


FIG. 30

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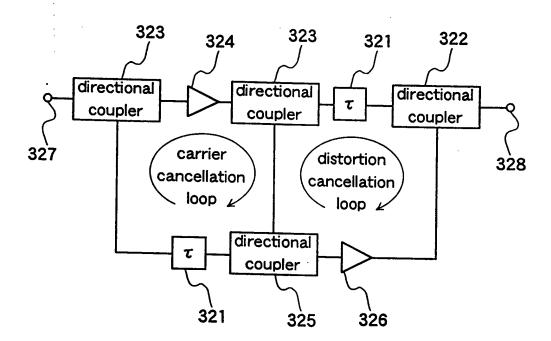


FIG. 32 PRIOR ART